

CAN First-TF Communauté Autour du Numérique de First-TF

<http://first-tf-can.utinam.cnrs.fr>
 Site web de la communauté
 Community web site



can@first-tf-can.utinam.cnrs.fr
 Liste de diffusion
 Mailing list

Contact : contact@first-tf-can.utinam.cnrs.fr

Wiki

Favoriser les échanges, développer des méthodes pour l'instrumentation temps-réel de haute précision.

Create exchange and develop high-precision digital methods and realtime instrumentation. Suggestion de thèmes abordés sur le site collaboratif type wiki - Topics available on the wiki-like collaborative platform :

- Fully digital signal analysis, quantification, noise
 - Servo controls
 - Digital Lock-in amplifiers
 - DDS & PLL
 - Atoms & objects manipulations
 - Time to Digital converters, Electronic counters
 - Noise generators, . . .
 - GPS systems
 - Time in Networks (White Rabbit, ...)
 - Software-defined Radio
 - Open-Source instruments
- Research, Amateur Radio, Education, Popularization

Champs des activités – Activity fields

-**Veille technologique** : techniques en électronique « numérique » appliquées aux aspects T/F, état de l'art, réflexions communes, nouvelles approches. **Technological watch**: T/F digital designs, state-of-the-art, joint discussions, new solutions.

-**Pédagogie** : portail web CAN (bibliographie, aide et initiations), développement de maquettes, support aux formations de FIRST-TF. **Training**: online portal CAN (bibliography, designs guides), prototyping, materials for FIRST-TF.

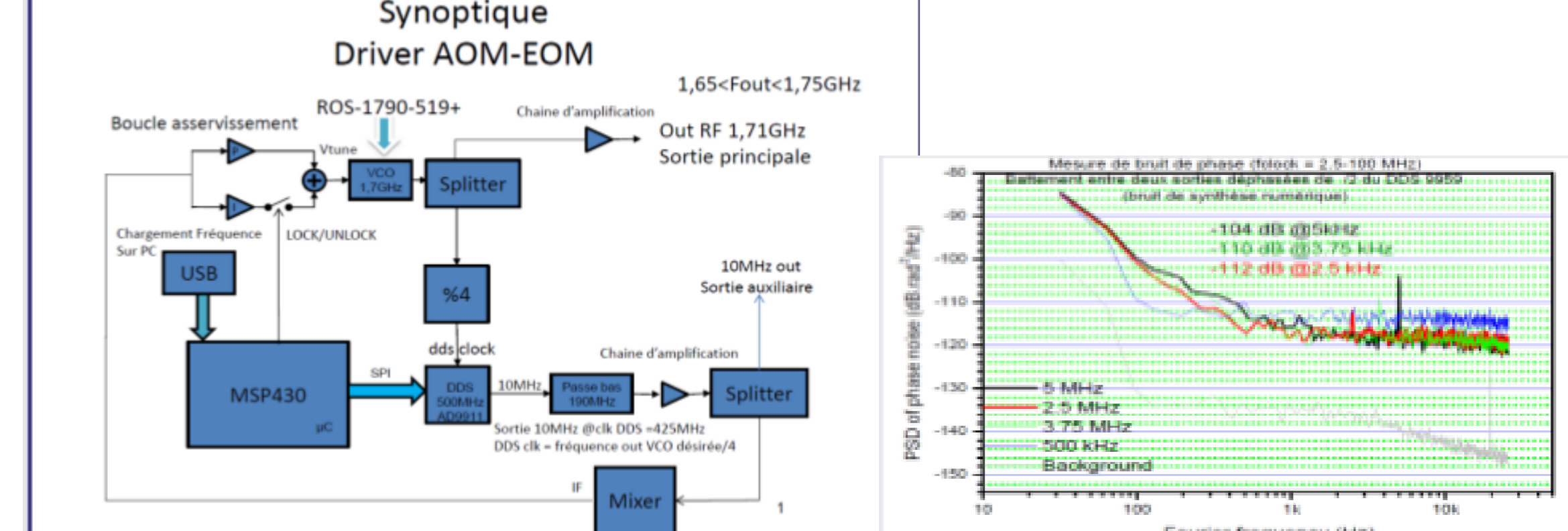
-**Echanges techniques** : wiki, liste de diffusion, organisation d'ateliers. **Experience sharing**: wiki, mailing list,, workshops organisations.

- **Soumissions de projets** pour financement par FIRST-TF. **Proposals** for financial support by FIRST-TF.

-**Publications techniques / Technical publications**

-**Favoriser les collaborations / Develop Collaborations**

Cold-atoms : AOM/EOM drivers



- AD9911 / AD9852-58-59
- SPI : MSP430
- Pc comm via USB
- E/S (freq/power)
- Bandwidths..

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Examples of Realizations

A DDS-based phase and frequency tracking system

- DUT & DDS are maintained in quadrature
- The controller tracks the phase and the frequency
- A low-cost experiment (Redpitaya or Zedboard with LTC2158, AD9915 eval board) provides easily 3 ms phase tracking and 0.7 s frequency rates
- Prefiltering stages within FPGA (e.g. Box car with 2¹⁸ averages lead to a time constant of 2 ms)
- Servos implemented either realtime or userspace depending on chosen sensitivity (allows for saving ressources).

For the sake of reconfigurability demonstration and ease development, we present here simple tests on medium quality quartz oscillator while data analysis is performed through the embedded CPU unit. Concurrent RAM transfer and calculations benefits from multithreaded techniques.

- red: spare free-running quartz oscillator (meas: HP53132A)
- green: stability measurement at DDS output
- blue: Frequency correction calculation